## DDS/MR1 Manual

For Sales or Service Contact:

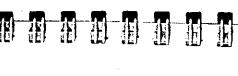
Data Connect Enterprise 301-924-7400

http://www.data-connect.com/ sales@data-connect.com



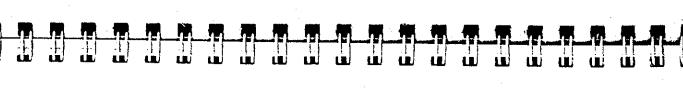
## DDS/MR1

Installation and Operation



Motorola UDS 5000 Bradford Drive Huntsville, AL 35805-1993 (205) 430-8000

> Doc. No. 6 456 5072 02 014 April 1994 • 1994 UDS



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### UPDATES

Updates to the products and the manual are obtainable at participating UDS dealers and distributors, or directly from UDS on the same terms and conditions as those offered by UDS to its registered customers as verified by the completion and return of the registration form to UDS.

## FCC REQUIREMENTS

This equipment complies with FCC Rules Part 68. Please note the following:

 When you order service, the telephone company needs to know the Facility Interface Code:

56KB-DDS	9.6KB-DDS	4.8KB-DDS	2.4KB-DDS	Type of Service
04DU5-56	04DU5-96	04DU5-48	04DU5-24	Facility Interface Code

- The Service Order Code: 6.0 Y
- The USOC jack required: RJ48S

In addition, if requested, please inform the telephone company of the make, model number, and FCC registration number, which are on the equipment label.

The telephone company may change technical operations or procedures affecting your equipment. You will be notified of changes in advance to give you ample time to maintain uninterrupted telephone service.

If you experience trouble with this telephone equipment, please contact

5000 Bradford Drive 5000 Bradford Drive Huntsville, Alabama 35805-1993 Telephone: (205) 430-8000

for information on obtaining service or repairs. The telephone company may ask that you disconnect this equipment from the network until the problem has been resolved. If your equipment continues to disrupt the network the telephone company may temporarily disconnect service. If this occurs you will be informed of your right to file a complaint with the FCC.

### WARNING

This equipment uses, generates, and can radiate radio frequency energy interfering with radio communications if not installed and used in accordance with the instruction manual. It has been tested and complies with the limits for a Class A computing device according to FCC Rules, Part 15. Operation of this equipment in a residential area may cause interference. If it does, you must correct the cause of the interference. Shielded cables may be necessary with this unit to ensure compliance with the Class A limits.

Changes or modifications to this unit not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

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# CANADIAN EMISSION REQUIREMENTS

This digital apparatus does not exceed the Class A limits for radio noise emissions from digital apparatus set out in the Radio Interference Regulations of the Canadian Department of Communications.

Le présent appareil numérique n'emet pas de bruits radioélectriques dépassant les limites applicables aux appareils numériques de la class A prescriptes dans le Règlement sur le brouillage radioélectrique édicté par le ministère des Communications du Canada.

### PREFACE

This manual is written for users of the UDS DDS/MR1. Please read it before you change any option on the printed circuit boards or operate the unit. This manual includes the following:

Chapter 1 Introduction - Contains introductory information and equipment description;

Chapter 2 Installation - Contains instructions for

Operation - Describes operational procedures and processes;

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mechanical and electrical installation of the

DDS/MR1;

Configuration - Describes front panel and hardware selectable options and how to apply them;

Diagnostics - Describes test data loops and features;

Chapter 5

Chapter 6

Chapter 4

Chapter 3

Troubleshooting - Describes tests and indications used to locate or isolate malfunctions;

Non-DDS Applications - Describes the use of the DDS/MR1 as a limited distance modem;

Maintenance - Contains maintenance information;

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Rate Adapter Card - Describes the use of the DDS/MR1 with the rate adapter piggyback card installed;

Appendix A

Chapter 8

Chapter 7

Appendix B Specifications;

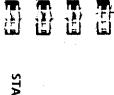
Appendix C Abbreviations and Acronyms.

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Warranty

Registration Card

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# STATEMENT OF APPLICATION

This manual supports both the standalone and shelf mount units. Operation and function of either unit is identical. Where necessary, this manual provides detailed information in support of the standalone unit. Detailed information in support of the shelf mount unit can be found in the shelf installation and operation manual.

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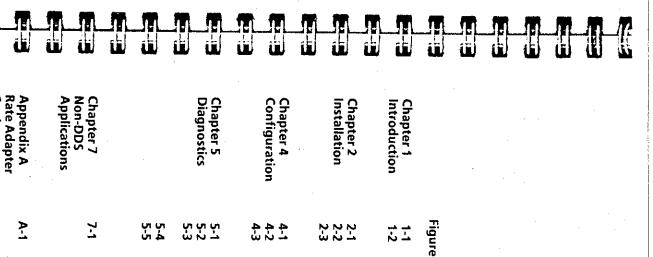
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GENERAL

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Rate Adapter

Appendix A

Α.

Rate Adapter Card Option

Menu

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FUNCTIONAL DESCRIPTION



### Introduction Chapter 1

specified in Bell publications 62310 and 41450. equipment and complies with the requirements compatible with Bell 500 Series DSU and CSU Service Unit (DSU) and a Channel Service Unit point-to-point or multipoint applications. The kbps for error free communication in either DDS/MR1 combines the functions of a Data (CSU) into a single compact device. It is directly The unit operates at all data rates from 1.2 to 64 connects a data terminal to the digital network. The UDS DDS/MR1 is a digital data unit that

pair cable systems. over privately owned 4- wire, unloaded, twisted synchronous or asynchronous data from 1.2 to operate as a limited distance modem providing Digital Data Service Network, the DDS/MR1 can full-duplex, serial communications with either 19.2 kbps, or synchronous data at 56 and 64 kbps In addition to offering DDS operation on the

synchronous or asynchronous while the link can over a 2.4 kbps link. The data can be either be either DDS or LDM. For more information on data at two rate adapted speeds: either 19.2 this mode of operation refer to Appendix A. card, the DDS/MR1 is also capable of transmitting When equipped with a rate adapter interface kbps data over a 56 kbps link or 1.2 kbps data

cable systems. The DDS/MR1 uses a bipolar as pulses on the communications line rather than return-to-zero signal. This system encodes data limited distance 4-wire, unloaded, twisted-pair transmission over the DDS Network or other asynchronous digital data from the DTE for The DDS/MR1 processes serial synchronous or

analog modems. modulating a carrier frequency as in standard

Liquid Crystal Display (LCD) allow the operator to nonvolatile memory to store selected settings. review or change settings. The DDS/MR1 uses the front panel pushbuttons. Prompts on the The DDS/MR1 can be configured entirely through

support 64 kbps or slower speeds. The DTE 232, CCITT V.35, and EIA-530/EIA-449. interfaces available for the DDS/MR1 include EIA-The DDS/MR1 DTE interface can be changed to

compensate for distortion and attenuation caused by line length without user adjustment. The receiver contains an automatic equalizer to

shelf mount unit.) The front panel (Figure 1-1) connects to the DDS line. The unit contains a contains a power cord, power switch, fuse, DTE pushbuttons, and an LCD screen. The rear panel contains six Light Emitting Diodes (LEDs), three available piggyback DTE interface pc boards. main printed circuit (pc) board and one of four Data Terminal Equipment and the 8-pin jack connector, and a standard 8-pin DDS line jack (Consult your dealer for information on the UDS (Figure 1-2). The DTE interface connects to the The DDS/MR1 is a standalone desktop unit.

DESCRIPTION PHYSICAL

design that uses customized LSI circuitry to conditioning of parts and assemblies. This is ensured by extensive tests and environmental followed by an acceptance test of the finished reduce the number of components. It is turther Reliability is ensured by a conservative electrical

RELIABILITY

customers with the best products possible. review and update in order to provide our The UDS Reliability Program is under continuous

1 

Introduction

SGB

S

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HOME 7

DDS/MR1

Figure 1-1 DDS/MR1

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8 Su J POWER (A) ş

Figure 1-2 Rear Panel

DDS/MR1

1.2







INSPECTION RECEIPT

























**PREPARATION** 

SITE



















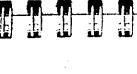
### Installation Chapter 2

material for future shipment. keep the shipping container and packing and UDS for advice and assistance. You should material shortage, contact the shipping agent occurred in shipment. If there is damage or equipment carefully for damage that may have contents against the packing list. Inspect the After unpacking the equipment, check the

terminal equipment. model and no further than 50 feet from the Vac grounded outlet as required for the specific Install the unit within 6 feet of a 115 or 230

signal line and interface cable clearance. Allow sufficient space at the rear of the unit for humidity, appreciable shock, and vibration. lighted, and free from extremes of temperature, The installation area should be clean, well

described in Chapter 4, Configuration. If a hard standard installations. The factory options are in Chapter 4 before continuing installation. option requires changing, follow the instructions operation. These options are factory set for be set to the correct DTE interface prior to the piggyback board has a dip switch that must verified or changed prior to installation. Also, The pc card has two strap options that should be



2 Installation

INSTALLATION

Figures 2-1 and 2-2 illustrate typical installations.

2 Installation

☐ Connect the DTE connector to the 25-pin receptacle. Secure the two screws to complete the connection.

☐ Connect the opposite end to the DTE port.

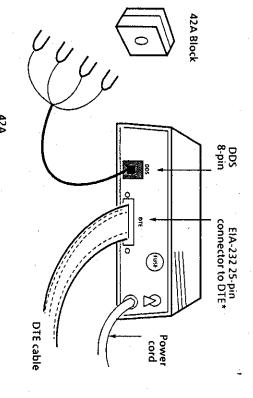
☐ Connect the 8-pin connector to the TELCO style DDS receptacle.

 Connect the opposite end to the TELCO DDS system.

Plug in the ac power cord

Place the power switch on.

The DDS/MR1 will now perform all functions as determined by option configuration (Chapter 4)



To central office Green Yellow Black 42A block Screw terminal 0 Spade 1 lug Ring (R1) 8 Ring (R) Tip (T1) Tip (T) Pin # 4-wire TX and fix 2-wire RX patr 4-wire uni d

\* The DTE piggyback board selected at time of purchase is equipped with the appropriate DTE interface.

Figure 2-1 Connection Using 42A Block

2.2



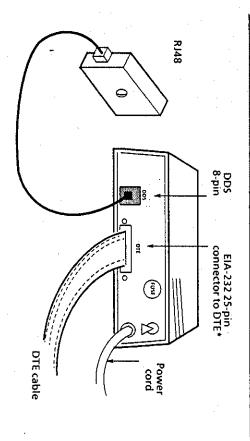


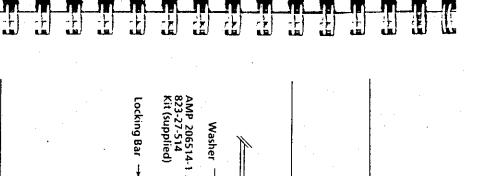
Figure 2-2 Connection Using RJ48 Jack

### Module V.35 Adapter

to the V.35 pc board. The following instructions apply for the standalone or shelf configuration. An adapter module field change kit is provided for EIA-232 DDS/MR1 units that are field changed

- Remove the two EIA-232 hex lug nuts with a 3/16 inch nut driver.
- Open the supplied hardware bag
- Using the same 3/16 inch nut driver, insert the screws and washers as shown in Figure 2-3.
- position. that the locking bar is in the open / load Insert the module over the DB-25 connector. If the module does not insert, check to see

1



DB-25 Female Connector

D8-25 Male

Open/Load Close/Lock

Adapter Module

Back of DDS/MR1

The module can be locked in place by sliding the bar to the right (close / lock).

Installation

Figure 2-3 Adapter Module Installation

V.35 Connector



GENERAL

**EQUIPMENT (DTE)** DATA TERMINAL INTERFACE



















CONNECTORS **DTE INTERFACE** 

Operation

Chapter 3

at the end of Chapter 4. The DDS/MR1 requires will perform all functions as optioned. configuration, turn the power on; the DDS/MR1 no start-up procedure. After installation and operation of the unit should be understood prior selected by the LCD and pushbuttons. Therefore, configuration options are soft options and are to option selection. Hard options are described operation and configuration. All but two After completing installation the unit is ready for

powerup. DTE piggyback board provided. The type of DTE 530/EIA-449, or CCITT V.35, depending on the 25-pin D-type connector. The sense levels and interface installed in the unit is displayed on impedances conform to either EIA-232, EIA-The DTE interface to the DDS/MR1 is through a

#### NOTE

interface card may not always operate properly at 56 or 64 kbps data rates. units equipped with an EIA-232 configured for all rates up to 64 kbps the EIA-232 standard specifies rates o allows the DDS/MR1 unit to be 19.2 kbps or less. Therefore, DDS/MR1 Although the EIA-232 interface card

using the EIA-449 interface. This can be done easily interconnected with terminal equipment equipped with the EIA-530 interface can be Pin functions of the DTE interface connector are listed in Tables 3-1, 3-2, and 3-3. A DDS/MR1

with an external crossover cable or other connecting device. The pin functions for both EIA-530 and EIA-449 are listed in Table 3-3.

There are 12 signals specified in the EIA-449 standard which are not supported by the DDS/MR1. These signals and pin numbers are listed in Table 3-4.

	10	** -12V
-	9	** + 12V
	22	*Test pattern
	11	*RT loopback
141/	18	*LL loopback
140/	21	*Remote loopback
113/DA	24	External clock
103/BA	2	Tx data
114/DB	15	Tx clock
115/DD	17	Rx clock
104/BB	. 3	Rx data
110/CG	13	**No signal
142/	25	**Test mode
109/CF	8	Received line signal detector
107/CC	6	Data set ready
106CB	5	Clear to send
105/CA	4	Request to send
102/AB	7	Signal ground
101/AA	1	Protective ground
CCITT/RS-232	Pin	Circuit Function

## Table 3-1 CCITT/EIA-232 Connector

- Can be disabled by front panel pushbuttons.
- \*\* Selected by DTE interface dip switches.

3 Operation

Circuit Function	"D" Connector	V.35 Connector
Protective ground		Α
Signal ground	7	В
Request to send	4	٠
Clear to send	5	g
Data set ready	6	E
Received line signal detector	8	j.
**Test mode	25	<b>X</b>
**No signal	12	Z
Rx data A	3	R
Rx data B	16	T
Rx clock A	17	٧.
Rx clock B	19	×
Tx clock A	15	Υ
Tx clock B	13	AA/à
Tx data A	2	Ρ
Tx data B	14	\$
External clock A	24	n
External clock B	23	W
*Remote loopback	21	8B/b
*LL loopback	18	J
*RT loopback	11	EE/f
*Test pattern	22	L
** + 12V	9	₹.VIT
**-12V	10	ινj

Table 3-2 V.35 Connector

- \* Can be disabled by front panel pushbuttons.
  \*\* Selected by DTE interface dip switches.

DIN         PIN         CCITT/ RS-530         Pin         R           1         101/         1         1           7         102/AB         19         19           4         105/CA (A)         7         R           19         105/CA (B)         25         R           5         106/CB (B)         27         C           6         107/CC (A)         11         D           6         107/CC (B)         29         D           11         10         109/CF (B)         31         R           11         10         109/CF (B)         31         R           19         115/DD (B)         24         R         R           19         115/DD (A)         8         R         R           19         115/DD (B)         24         R         R           11         114/DB (A)         5         S           12         114/DB (A)         4         S           22         103/BA (B)         22         S           11         113/DA (A)         17         T           12         113/DA (A)         17         T           18	R	14	140/RL	21	Remote loopback
rection         Pin         CCITT/ RS-530         Pin           1         101/         1           1         101/         1           1         7         102/AB         19           1         1         105/CA (A)         7           1         1         105/CA (B)         25           1         1         105/CA (B)         25           1         1         106/CB (B)         27           1         1         106/CB (B)         27           2         107/CC (A)         111         1           8         109/CF (A)         111         1           9         109/CF (B)         31         1           10         14/BB (A)         6         1           11         115/DD (A)         8         24           15         114/DB (B)         24           12         114/DB (A)         5           14         103/BA (B)         23           17 <t< td=""><td>רר</td><td>10</td><td>141/LL</td><td>18</td><td>*LL loopback</td></t<>	רר	10	141/LL	18	*LL loopback
wit Function         Pin         CCITT/ RS-530         Pin           ee ground         1         101/         1           ound         7         102/AB         19           to send A         4         105/CA (A)         7           to send B         19         105/CA (B)         25           send B         19         105/CA (B)         25           send B         13         106/CB (B)         25           send B         13         106/CB (B)         25           send B         13         106/CB (B)         27           ready A         6         107/CC (A)         111         1           ready B         22         107/CC (B)         29         1           line signal         8         109/CF (A)         13         1           tor A         3         104/BB (A)         6         31           ode / no signal         25         142/Fm110/         18         24           A         17         115/DD (A)         8         24           A         15         114/DB (B)         26           A         15         114/DB (B)         23           A         2	11 (B)	35	113/DA (B)	=	External clock B
wit Function         Pin         CCITT/ RS-530         Pin           re ground         1         101/         1           ound         7         102/AB         19           to send A         4         105/CA (A)         7           to send B         19         105/CA (B)         25           send B         19         105/CA (B)         25           send B         13         106/CB (B)         25           send B         13         106/CB (B)         25           send B         13         106/CB (B)         27           ready A         6         107/CC (A)         11         1           ready B         22         107/CC (B)         29         1           line signal         8         109/CF (A)         13         1           tor B         16         104/BB (A)         6         3           line signal         25         142/Fm110/         18         6           line signal         25         142/Fm10/         8         24           lor B         16         104/BB (B)         24         8           lor B         16         104/BB (B)         24         8	TT (A)	17	113/DA (A)	24	External clock A
it Function         Pin         CCITT/ RS-530         Pin           e ground         1         101/         1           ound         7         102/AB         19           o send A         4         105/CA (A)         7           o send B         19         105/CA (B)         25           end A         5         106/CB (A)         9           end A         6         107/CC (A)         11         1           eady A         6         107/CC (A)         11         1           eady B         22         107/CC (A)         11         1           eady B         22         107/CC (B)         29         1           line signal or B         10         109/CF (A)         13         1           or B         3         104/BB (A)         6         31           ode/no signal         25         142/Tm 110/         18         6           16         104/BB (A)         6         6         24           17         115/DD (B)         26         24           15         114/DB (A)         5         114/DB (B)         23           12         103/BA (A)         4         4	SD (B)	22	103/BA (B)	14	Tx data B
it Function         Pin         CCITT/ RS-530         Pin           e ground         1         101/         1           ound         7         102/AB         19           o send A         4         105/CA (A)         7           o send B         19         105/CA (B)         25           end A         5         106/CB (A)         9           end B         13         106/CB (B)         27           eady B         6         107/CC (A)         11         1           eady B         22         107/CC (B)         29         1           line signal or B         10         109/CF (A)         13         1           or B         10         109/CF (B)         31         1           ode/no signal         25         142/Tm 110/         18         6           de/no signal         25         142/Tm 110/         18         6           3         104/BB (A)         6         6         24           4         15         114/DB (B)         24           5         114/DB (B)         25         114/DB (B)         25	SD (A)	4	103/8A (A)	2	Tx data A
ift Function         Pin         CCITT/ RS-530         Pin           e ground         1         101/         1           o send A         4         105/CA (A)         7           o send B         19         105/CA (B)         25           end A         5         106/CB (A)         9           end B         13         106/CB (B)         27           eady A         6         107/CC (A)         11         1           eady B         22         107/CC (B)         29         1           line signal ine signal or A         10         109/CF (B)         31         3           or B         10         109/CF (B)         31         3           or B         16         104/BB (A)         6         31           or B         3         104/BB (A)         6         31           or B         16         104/BB (B)         24         6           or B         16         104/BB (B)         24         6           or B         16         104/BB (B)         24         6           or B         16         115/DD (B)         26         6           or B         16         114/DB	ST (B)	23	114/DB (B)	12.	Tx clock B
it Function         Pin         CCITT/ RS-530         Pin           e ground         1         101/         1           ound         7         102/AB         19           o send A         4         105/CA (A)         7           o send B         19         105/CA (B)         25           end A         5         106/CB (A)         9           end B         13         106/CB (B)         27           eady A         6         107/CC (A)         11         1           eady B         22         107/CC (B)         29         1           line signal         8         109/CF (A)         13         1           or B         10         109/CF (B)         31         1           ode / no signal         25         142/Tm 110/         18         1           ode / no signal         25         142/Tm 110/         18         6           16         104/BB (A)         6         6         1           24         15/DD (B)         24         1           3         115/DD (B)         26         1	ST (A)	5	114/DB (A)	15	Tx clock A
it Function         Pin         CCITT/ RS-530         Pin           a ground         1         101/         1           b ground         7         102/AB         19           o send A         4         105/CA (A)         7           o send B         19         105/CA (B)         25           end A         5         106/CB (A)         9           end B         13         106/CB (B)         27           eady B         6         107/CC (A)         11         1           eady B         22         107/CC (B)         29         1           line signal or A         8         109/CF (A)         13         1           or B         10         109/CF (B)         31         1           ode/no signal or B         25         142/fm 10/         18         1           de/no signal or B         25         142/fm 10/         18         1           de/no signal or B         25         142/fm 10/         18         1           de/no signal or B         25         142/fm 10/         18         1           de/no signal or B         25         142/fm 10/         18         1           de/no signal or	RT (B)	26	115/DD (B)	9	Rx clock B
Pin         CCITT/ RS-530         Pin           1         101/         1           7         102/AB         19           4         105/CA (A)         7           19         105/CA (B)         25           5         106/CB (A)         9           6         107/CC (A)         11         1           22         107/CC (B)         29         1           8         109/CF (A)         13         1           10         109/CF (B)         31         1           3         104/BB (A)         6         1           16         104/BB (B)         24         6	RT (A)	8	115/DD (A)	17	Rx clock A
n         Pin         CCITT/ RS-530         Pin           1         101/         1           7         102/AB         19           4         105/CA (A)         7           19         105/CA (B)         25           5         106/CB (B)         25           13         106/CB (B)         27           6         107/CC (A)         11           22         107/CC (B)         29           22         109/CF (A)         13           8         109/CF (B)         31           10         109/CF (B)         31           3         104/BB (A)         6	RD (B)	24	104/88 (B)	16	Rx data B
Pin CCITT/ RS-530 Pin 101/ 1 101/ 1 1 101/ 1 1 101/ 1 1 1 101/ 1 1 1 1	RD (A)	6	104/BB (A)	3	Rx data A
Pin CCITT/ RS-530 Pin 1 101/ 1 1 101/ 1 1 105/CA (A) 7 19 105/CA (B) 25 5 106/CB (A) 9 5 106/CB (B) 27 6 107/CC (A) 11 22 107/CC (B) 29 8 109/CF (A) 13	MI	18	142/Tm110/	25	**Test mode / no signal
Pin CCITT/ RS-530 Pin 1 101// 1 7 102/AB 19 4 105/CA (A) 7 19 105/CA (B) 25 5 106/CB (A) 9 13 106/CB (B) 27 6 107/CC (A) 11 1 8 109/CF (A) 13	RR (B)	31	109/CF (B)	10	Received line signal detector B
ction         Pin         CCITT/ RS-530         Pin           nd         1         101/         1           7         102/AB         19           IA         4         105/CA (A)         7           IB         19         105/CA (B)         25           IB         5         106/CB (A)         9           I3         106/CB (B)         27           A         6         107/CC (A)         11           22         107/CC (B)         29	RR (A)	13	109/CF (A)	8	Received line signal detector A
rition         Pin         CCITT/ RS-530         Pin           nd         1         101/         1           7         102/AB         19           A         4         105/CA (A)         7           B         19         105/CA (B)         25           5         106/CB (A)         9           13         106/CB (B)         27           6         107/CC (A)         11         1	DM (B)	29	107/CC (B)	22	Data set ready 8
rction         Pin         CCITT/ RS-530         Pin           und         1         101/         1           dA         7         102/AB         19           dB         19         105/CA (A)         7           dB         19         105/CA (B)         25           s         106/CB (A)         9           13         106/CB (B)         27	DM (A)	11	107/CC (A)	6	Data set ready A
tion         Pin         CCITT/ RS-530         Pin           nd         1         101/         1           7         102/AB         19           A         4         105/CA (A)         7           B         19         105/CA (B)         25           5         106/CB (A)         9	CS (B)	27	106/CB (B)	13	Clear to send B
tion         Pin         CCITT/ RS-530         Pin           nd         1         101/         1           7         102/AB         19           A         4         105/CA (A)         7           B         19         105/CA (B)         25	CS (A)	9	106/CB (A)	5	Clear to send A
on Pin CCITT/ RS-530 Pin 1 101/ 1 7 102/AB 19 4 105/CA(A) 7	RS (B)	25	105/CA (B)	- 61	Request to send B
on Pin CCITT/ RS-530 Pin 1 101/ 1 7 102/AB 19	RS (A)	7	105/CA (A)	4	Request to send A
on Pin CCITT/ RS-530 Pin	56	19	102/AB	7	Signal ground
Pin CCITT/ Pin	1	1	101/	1	Protective ground
	RS-449	Pin	CCITT/ RS-530	Pin	Circuit Function

Table 3-3 CCITT/EIA-530 or EIA-449 Connector

Can be disabled by front panel pushbuttons

\*\* Selected by DTE interface dip switches.

Standby indicator Signal quality Select frequency / signal rate selector Signaling rate indicator New signal Select standby incoming call (ring indicate) end common Terminal in service Receive common ferminal ready erminal ready **Circuit Function** 20 퍍 **υ** 3 30 28 4 6 5 7 RS-449 SF/SR õ Š RC 됬 ᆽ ∽ æ X S ሯ  $\bar{\cap}$ 

3 Operation

Table 3-4
EIA-449 Signals Not Supported By DDS/MR1

DDS/MR1 is in transmit mode and responds by Originated by DTE. When RTS is on, the turning on Clear to Send.

Request to Send RTS

Clear to Send

off the DDS/MR1 ignores input data. selected. For more details refer to Configuration to CTS on delay depends on the data rate (refer to Table 3-5). When the EIA-232 interface card is will turn off within 1 bit time. The normal RTS on going on. When CTS is on, the DDS/MR1 is ready to transmit data. When RTS is turned off, CTS Originated by the DDS/MR1 in response to RTS Option Descriptions in Chapter 4. When CTS is installed, longer RTS-to-CTS delay times can be

behavior of CTS may depend on the status of the Circuit Assurance option (CA OPT). Refer to Circuit Assurance in Chapter 4. Once RTS is raised by the DTE, the

Data Rate	RTS-CTS Delay
1200	sw 8.0 ± 91
2400	8 ± 0.4 ms
4800	4 ± 0.2 ms
9600	2 ± 0.1 ms
19.2k	1 ± 0.05 ms
56k	0.40 ± 0.02 ms
64k	0.35 ± 0.02 ms

CTS On Delay

Transmitter Clock

transmitter clock must not be less than 25% of samples the transmit data during the transition Originated by DDS/MR1. Provides the DTE with the nominal bit time. time between transition of the transmitter data transmit timing information. The DDS/MR1 line and the sampling transition of the from space to mark of the transmitter clock. The

Transmit Data

data to be transmitted Originated by DTE. Provides the DDS/MR1 with

External Clock

into the buffer when the buffer option is within  $\pm 0.1\%$  of required frequency. enabled. When external clock is used it must be be used in non-DDS applications, or to clock data transmit timing information, an option that may Originated by DTE. Provides the DDS/MR1 with

Signal Detector Received Line

has been received on the communication line Originated by DDS/MR1. Indicates valid data Also called carrier detect.

RLSD goes off under any of these conditions:

- Reception of 7 consecutive "Out-of-Service" Reception of 3 consecutive "IDLE" characters
- Loss of signal

When RLSD is off, Receive Data is held to a mark

Receive Clock RC

during the transition from space to mark of the receive clock.

**Receive Data** 

Data Set Ready DSR

No Signal NS

GROUNDING

Ground **Protective** 

Signal

Ground

**TEST SIGNALS** DTE INITIATED

> with continuous timing information for clocking Originated by the DDS/MR1. Provides the DTE received data. The DTE samples received data

3 Operation

of the nominal bit time. This signal is held in a line. Transitions of this lead occur within ±25% with data received from the communications Originated by the DDS/MR1. Provides the DTE mark state when RLSD is off.

that the DDS/MR1 is powered on Originated by the DDS/MR1. Indicates to the DTE

Chapter 4. the status of the System Status option (SS OPT). The behavior of DSR may depend on Refer to System Status in

from the DDS line. Originated by the DDS/MR1. Indicates no signal

Protective / chassis ground is provided on the DTE interface connector.

connection provides chassis ground the interface signals. An optional strap Signal ground provides a common reference for

also be DTE initiated. The DTE pin numbers used and 3. This section describes sequential signal DTE interface used and are listed in Tables 3-1, 2, to initialize these tests depend on the type of In addition to front panel initiation, tests can

3-6

DDS/MR1

mode is selected and the LCD shows the status of network. When lit; the TM LED indicates test generation that activates these tests. Test signals are looped between the DTE, DCE, and the DDS the selected test.

Local Line Loopback

also connected to loop signals back to the DDS communication line. communications transmitter and receiver are to loop signals back to the DTE. The transmitter and receiver connect internally When the DTE turns LL on, the DDS/MR1 logic

Loopback Remote Terminal

connecting the DTE transmit and receive data interface. When bilateral loopback is enabled data to and from the DDS line through the DTE When the DTE turns RT on, the DDS/MR1 loops this mode also provides a loopback path for

Remote Loopback

DDS/MR1 canceling the RT loopback command go into RT loopback. When RL is turned off, the DDS/MR1 sends a command to the remote command to the remote DDS/MR1 causing it to When the DTE turns RL on, the DDS/MR1 sends a

**Test Pattern** 

back through the DDS/MR1 receive logic. This through the DDS/MR1 transmit logic and looped when TP is on, the test pattern is transmitted message to be displayed. If the DDS/MR1 is in LI scanned for the same test pattern. Any error in the received pattern causes the appropriate is sent to the DDS line. The data received is results in a self test. When the DTE turns TP on, a 511 bit test pattern

Test Mode

When the DDS/MR1 is in either remote or local test mode, TM lights.

Not available with the EIA-530/EIA-449 DTE

**CONTROLS AND FRONT PANEL** 

INDICATORS

Pushbuttons

Operation 3

4

DDS SYSTEM INTERFACE

without a shorting bar as shown in FCC Rules and system consists of four leads divided to form a A mating connector is mounted on the DDS/MR1 leads are on a miniature 8-position jack (RJ48) and (d) (2). The remaining pins are not used. Regulations Part 68, Subpart F, Figures 68.500 (d) receive data pair and a transmit data pair. The Connection between the DDS/MR1 and the DDS

of these interface lines conform to AT&T are listed in Table 3-6. Technical Reference Pub 62310. Pin assignments The sense levels, voltage levels, and impedances

	6	9-2 older	
Brown	7 and 3	Line to DDS/MR1	Receive tip (T)
Slate	8 and 6	Line to DDS/MR1	Receive ring (R)
Blue	1 and 4	DDS/MR1 to line	Transmit ring (R1)
Orange	2 and 5	DDS/MR1 to line	Transmit tip
UDS Wire Color	Pin Number	Direction	Function

Pin Assignments

equalizer to compensate for any length DDS line. The receiver incorporates an automatic line

pushbuttons on the front panel. They allow the mode. user to configure the DDS/MR1 or select a test Configuration control is through three The three pushbuttons are

YES-Selects the displayed menu option

<u>N</u> Advances the displayed menu option

HOME mode Switches between Data mode and Set

DDS/MR1

DDS/MR1

**3-8** 

known as soft strapping. Configuration control through the front panel is

pushbutton manipulation. status or option changes resulting from The front panel ten character LCD displays the

operations or tests. interface signals resulting from modem The six front panel LEDs reflect status of the data

**LEDs** 

9

The LEDs are described as follows:

FM - On when the DDS/MR1 is in test mode

Blinks at one half second rate when placed in DSU loopback by the TELCO

by the DSU unit at the opposite end Blinks at one second rate when placed in RL

Blinks at two second rate when placed in CSU local loopback (CSULL) by the TELCC.

OS - On when the Out-of-Service code is received. This code is sent by TELCO during

- NS On when there is No Signal from the DDS line or no connection to the DDS line.
- CS On when the DTE CTS is on
- RD Indicates that Received Data is going to the DTE. On for a space, off for a mark.
- TD Indicates that Transmitted Data from the DTE is being sent. On for a space, off for a

CONFIGURING THE DDS/MR1

A rear panel power switch controls power on /

**GENERAL** 

Configuration Chapter 4

options are the same for the rate adapter card. For more details refer to Appendix A. conforms to EIA-232. Not all configuration asynchronous data to be transmitted over the 2.4 kbps DDS link. The rate adapter card interface piggyback card allows 19.2 kbps synchronous or different DTE interface. A fourth rate adapter kbps DDS link or 1.2 kbps synchronous or asynchronous data to be transmitted over the 56 Three piggyback cards are available, each with a

### NOTE

site is a relatively simple task when piggyback card preinstalled at the required factory. However, changing cards on The unit is purchased with the selected

synchronous is selected, bits per word does not an option is not available. For instance, if panel pushbuttons. Option descriptions state if Configuration options are selected by front

the LCD displays ERROR. If an error occurs ensure DDS/MR1 will perform a self test. If the test fails, it is consistent and then refer to Maintenance. After installation, turn the power on. The

**Power Switch** 



3-10